

FIG. 1

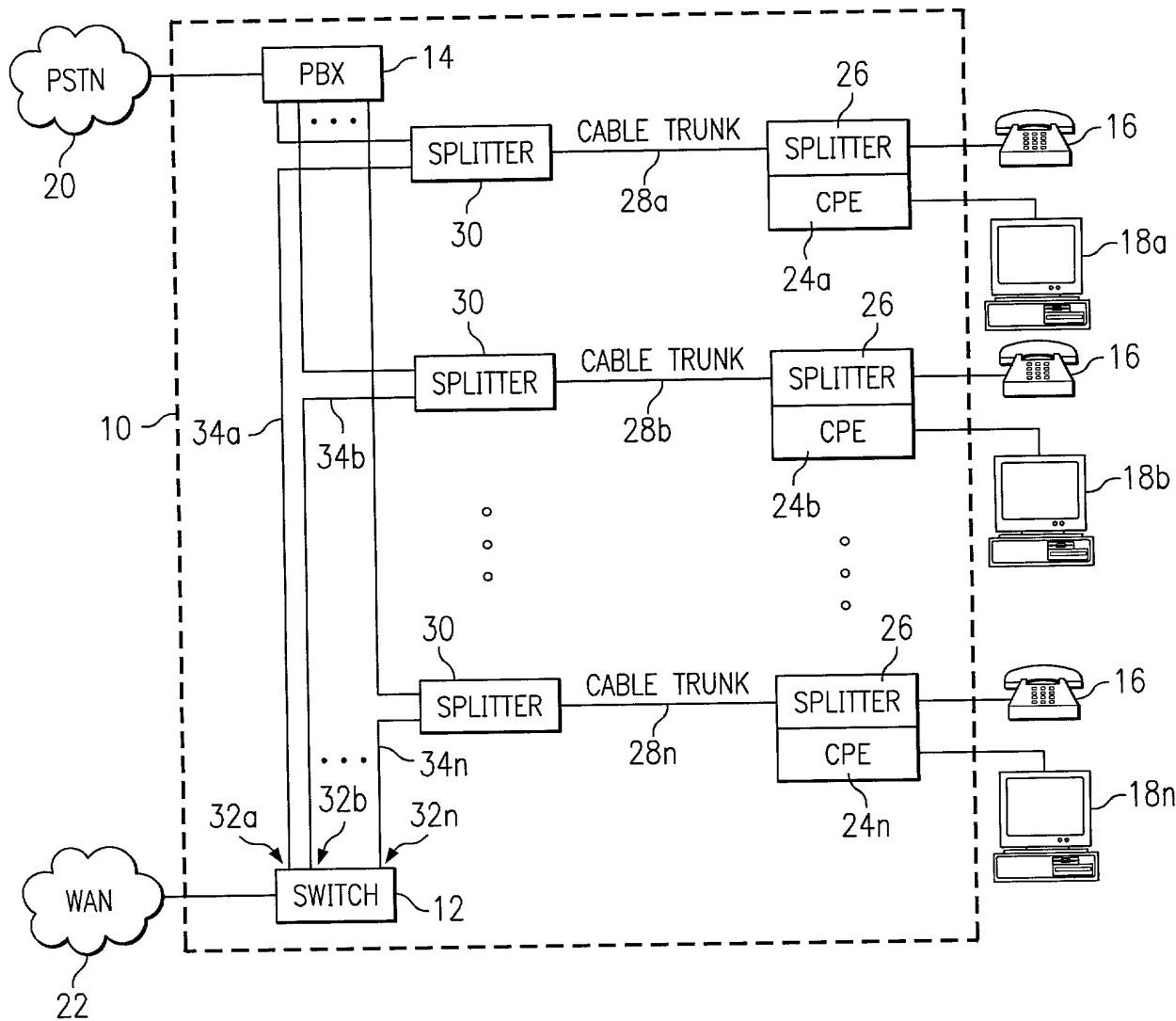


FIG. 2

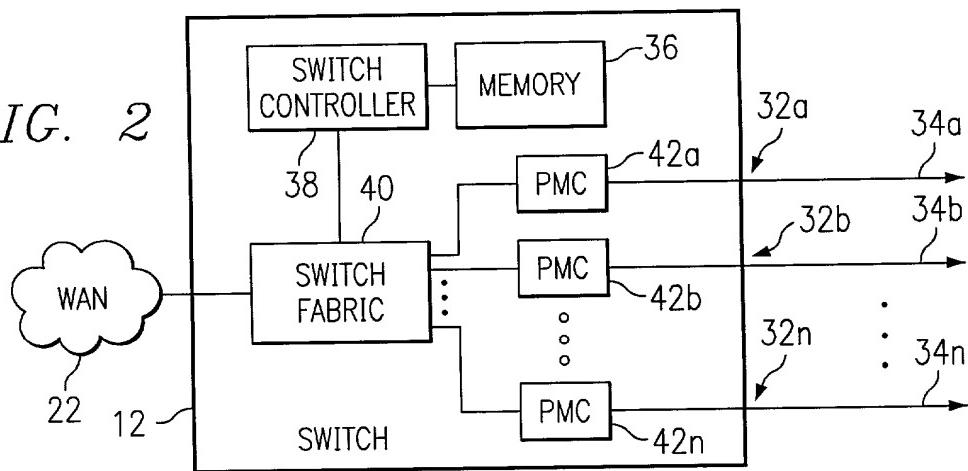


FIG. 3

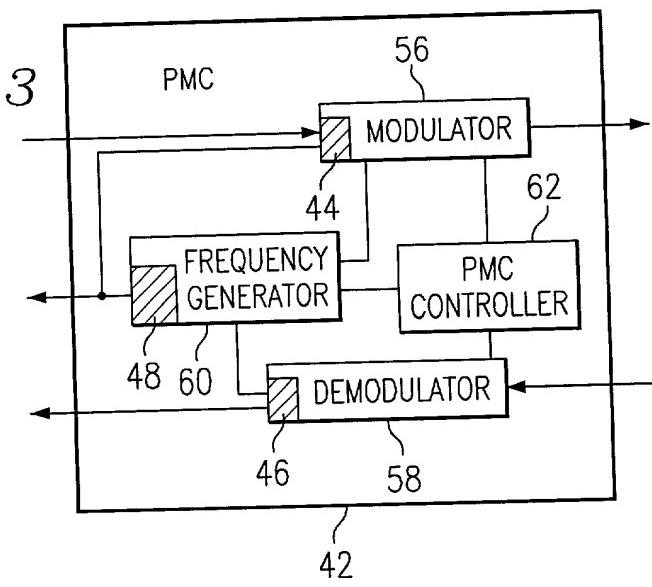


FIG. 4

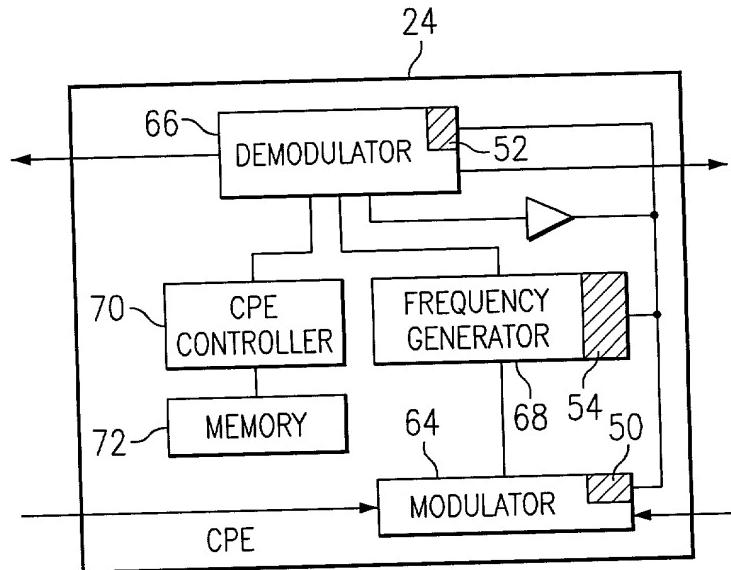


FIG. 5A

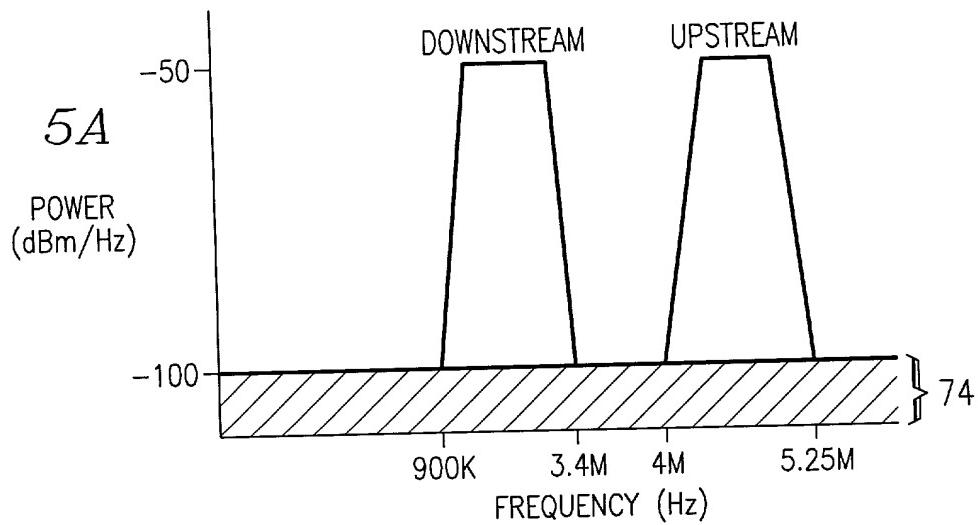


FIG. 5B

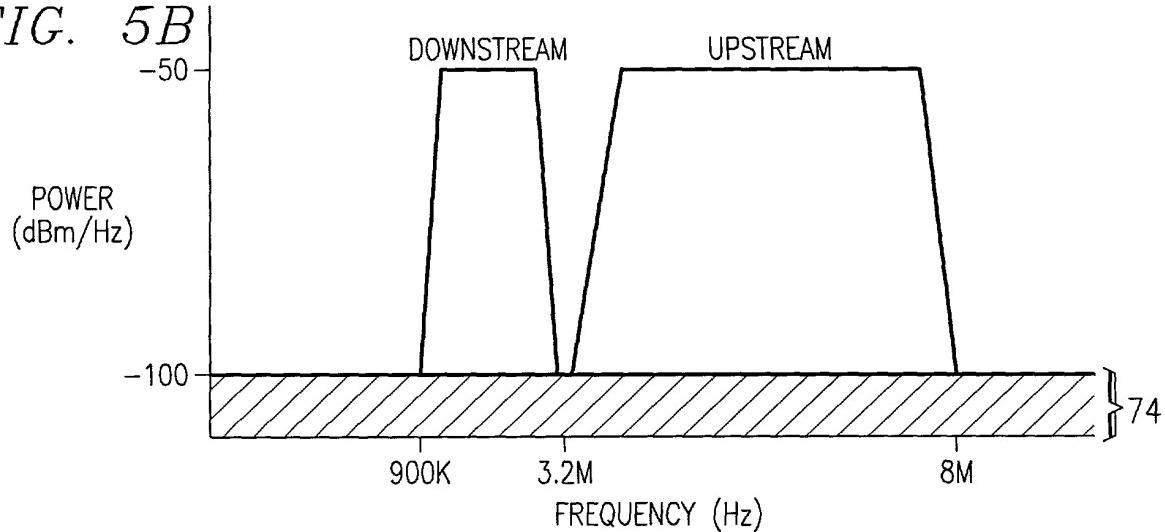


FIG. 5C

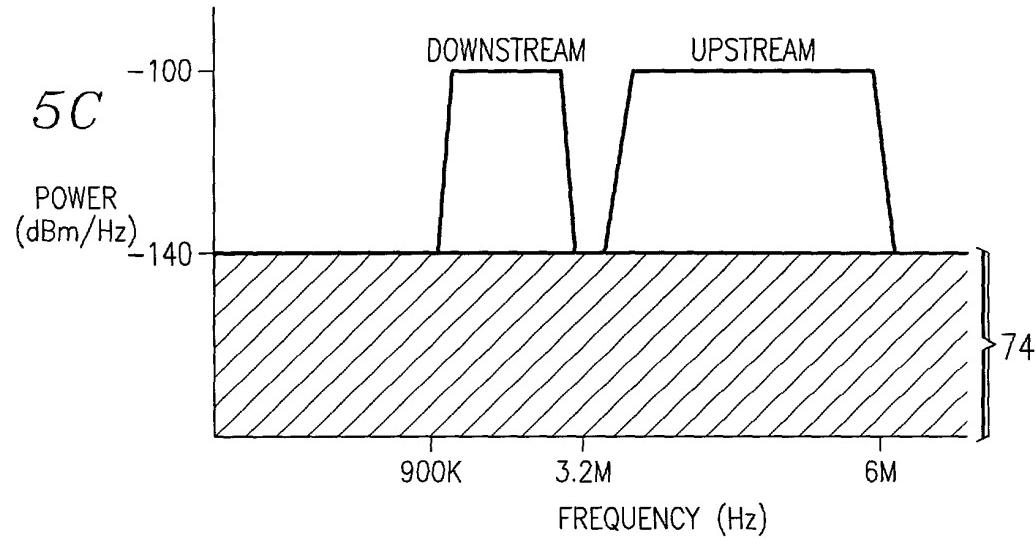


FIG. 5D

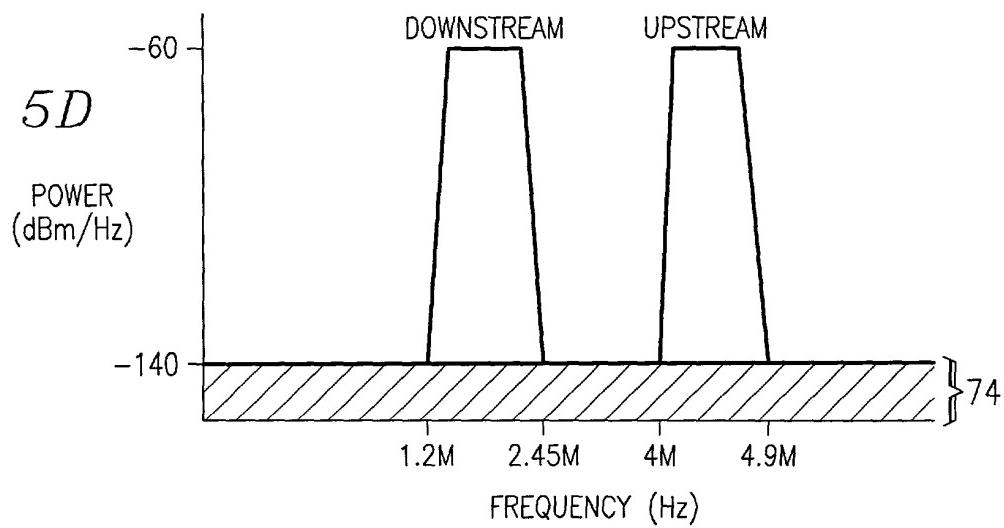


FIG. 6A

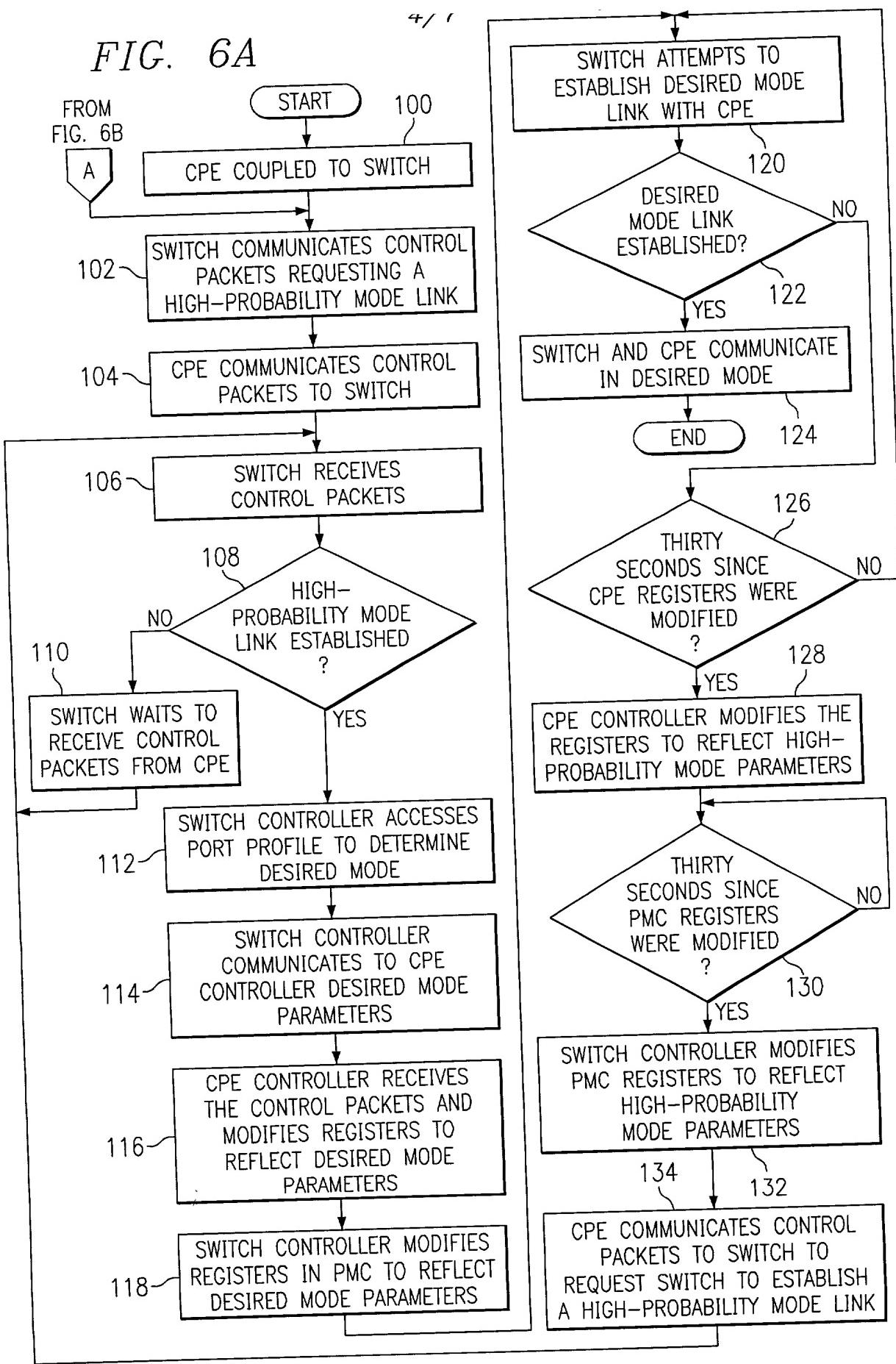
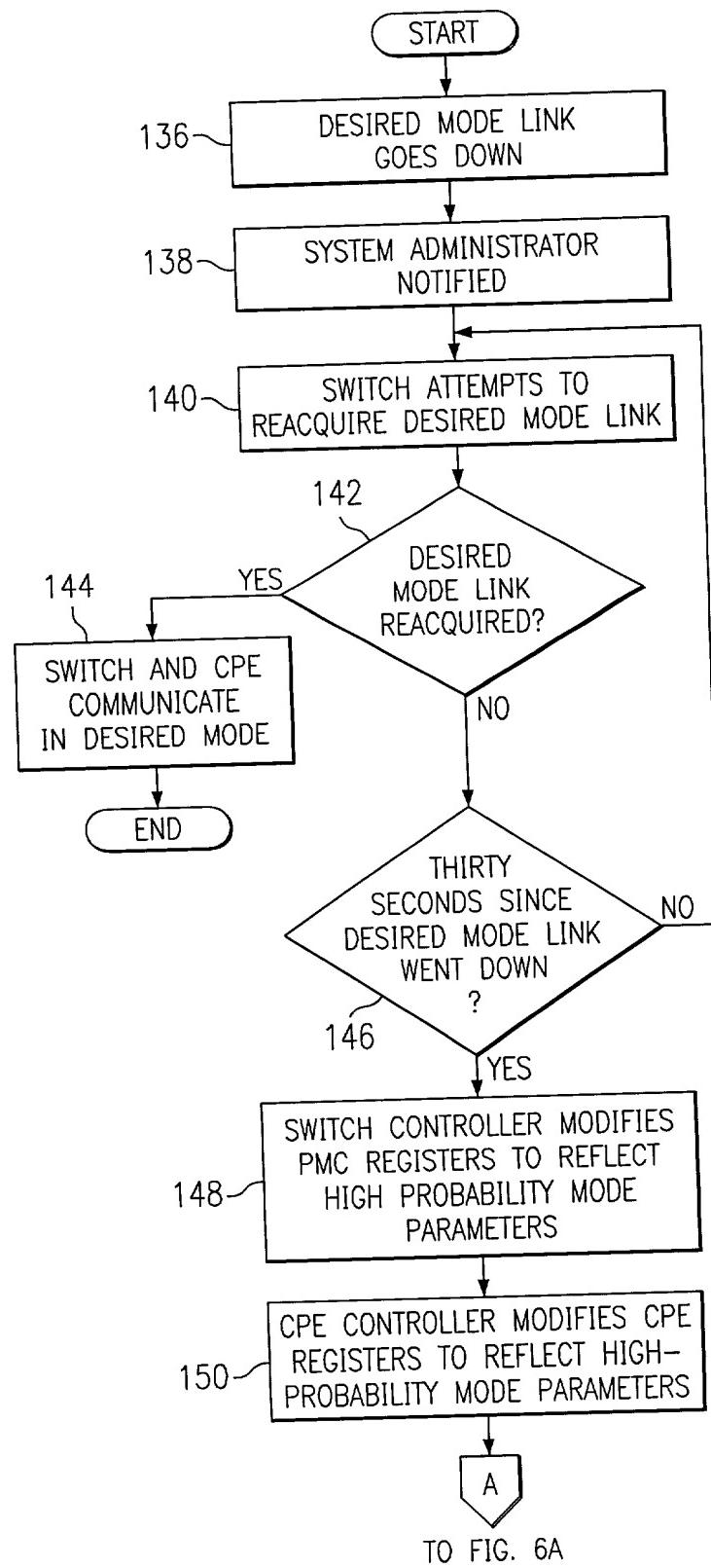
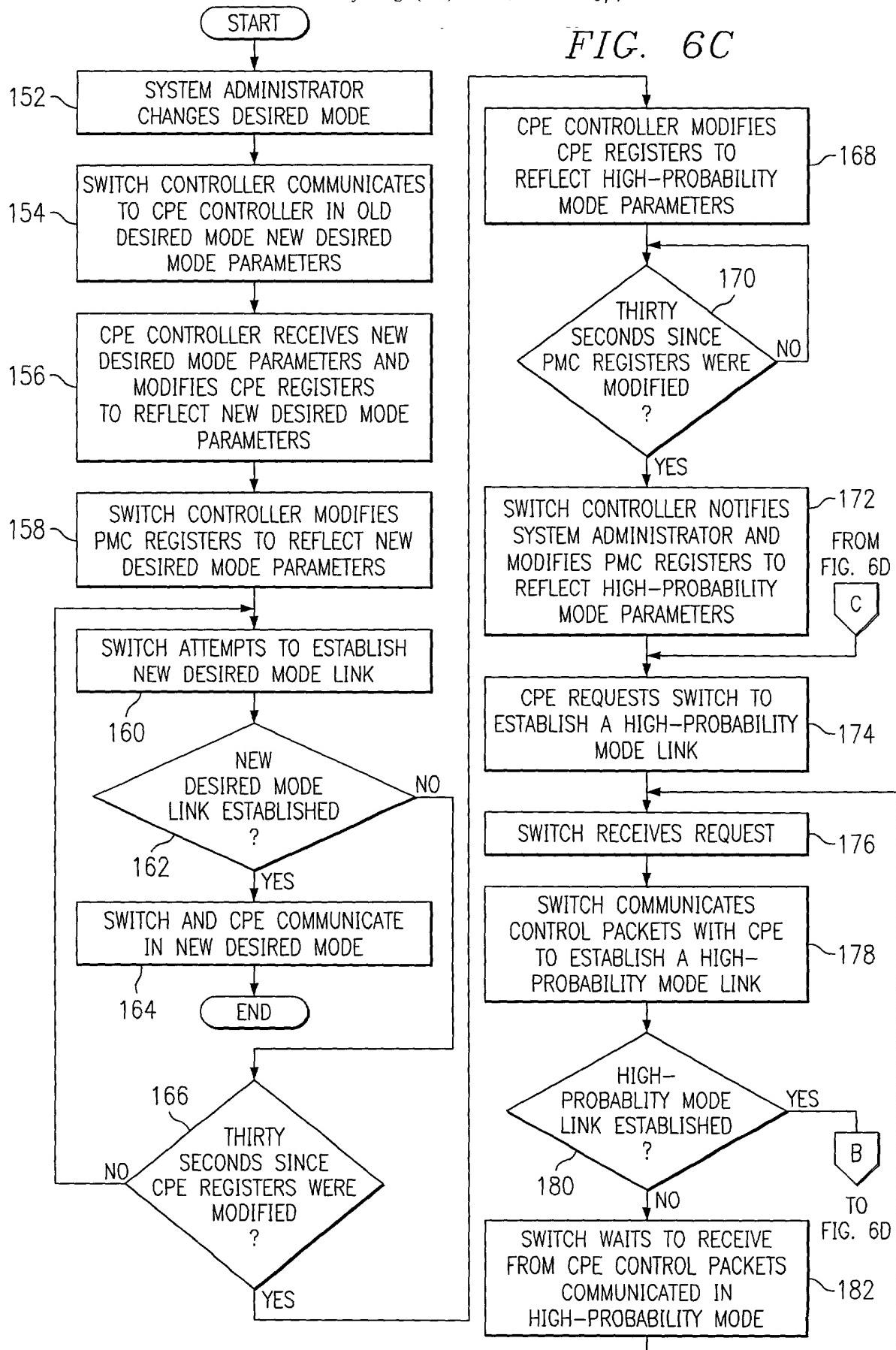


FIG. 6B



TO FIG. 6A



FROM
FIG. 6C
B

184 ~
SWITCH CONTROLLER
COMMUNICATES TO CPE OLD
DESIRED MODE PARAMETERS

186 ~
CPE CONTROLLER RECEIVES OLD
DESIRED MODE PARAMETERS AND
MODIFIES CPE REGISTERS TO
REFLECT OLD DESIRED MODE
PARAMETERS

188 ~
SWITCH CONTROLLER MODIFIES
PMC REGISTERS TO REFLECT
OLD DESIRED MODE PARAMETERS

190 ~
SWITCH ATTEMPTS TO
ESTABLISH OLD DESIRED MODE
LINK WITH CPE

OLD
DESIRED MODE
LINK ESTABLISHED ?
192 ~
NO

194 ~
SWITCH AND CPE COMMUNICATE
IN OLD DESIRED MODE

END

FIG. 6D

196 ~
THIRTY
SECONDS SINCE
CPE REGISTERS WERE
MODIFIED ?
YES

198 ~
CPE CONTROLLER MODIFIES CPE
REGISTERS TO REFLECT HIGH-
PROBABILITY MODE PARAMETERS

200 ~
THIRTY
SECONDS SINCE
PMC REGISTERS WERE
MODIFIED ?
YES

202 ~
SWITCH CONTROLLER
MODIFIES PMC REGISTERS TO
REFLECT HIGH PROBABILITY
MODE PARAMETERS

C
TO
FIG. 6C